## Notice of References Cited Application/Control No. 09/903,059 Examiner Dana Farahani Applicant(s)/Patent Under Reexamination BULUCEA, CONSTANTIN Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5399893	03-1995	Weitzel et al.	257/355
	В	US-5497028	03-1996	Ikeda et al.	257/531
	С	US-6166404	12-2000	Imoto et al.	257/279
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP406061446A	03-1994	JPO	TANI	
	0					
	Р					
	Q					
	R					
	s					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Adel S. Sedra and Kenneth C. Smith, Microelectronic Circuits 1998, Oxford University Press, Inc., 4 <sup>th</sup> edition, P. 382			
	٧				
	w				
	x				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.